ABSTRACT

A synthesizer arrangement for generating signals simultaneously, the arrangement comprising as an input a frequency reference signal generated with stable crystal oscillator means. The arrangement comprises first synthesizer means arranged to independently generate a first signal from the frequency reference signal, and as their input a first control signal controlling the generation, on the basis of which the first signal is modified independently, and second synthesizer means arranged to independently generate a second signal from the frequency reference signal, and as their input a second control signal controlling the generation, on the basis of which the second signal is modified independently. The first and the second synthesizer means comprise a digital fractional-N frequency divider for feedback, the frequency divider being controlled with a bit word which is arranged to be generated by means of a digital sigma-delta calculation circuit, whose input is one of said first and second control signals, which is for example a frequency correction signal or a frequency transfer signal.

(Fig. 1)